## Errata of

## Embedded SoPC Design with Nios II Processor and Verilog Examples

(Last Updated 08/01/2012)

• p.377, Figure 15.19: wrong image was embedded: the correct one should be

🖄 Component Editor - chu_avalon_div_demo_hw.tcl*	
File Templates	
Introduction HDL Files Signals Interfaces HDL Parameters Library Info	
About Interfaces	
"div_intr" (Interrupt S	Sender) 📃 🔺
Name: c	div_intr Info
Type:	nterrupt Sender
Associated Clock:	lock reset
	_
🕆 Block Diagram	
irq⊂- <mark>div_intr</mark>	
T Parameters	
Associated addressable interface: div_cpu	
- Waveform	
irq +	
•	• • • • • • • • • • • • • • • • • • •
Add Interface Remove Interfaces With No Signals	
Info: No errors or warnings.	
Нер	♦ Prev Next ▶ Finish

• The "←" symbols of RT operations in the ASMD chart are missing in p.157, Figure 7.2(a), due to printer's font problem; the correct diagram should be



- The "←" symbols of RT operations in the ASMD charts are also missing in the following figures:
  - Figure 7.3(a) in p.159.
  - Figure 7.4 in p.160.
  - Figure 7.6 in p.162.
  - Figure 7.7 in p.167.
  - Figure 7.8 in p.169.
  - Figure 7.11 in p.179.
  - Figure 17.2 in p.426.
  - Figure 17.5 in p.431.
  - Figure 21.1 in p.666.
  - Figure 22.4.1 in p.690.